2SA1864



Muting Circuits, Driver Applications

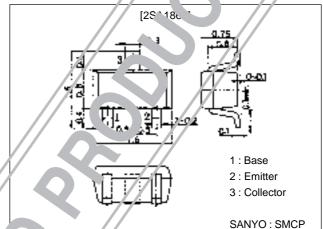
Features

- · On-chip bias resistors (R1=4.7k Ω , R2=4.7k Ω).
- · Very small-sized package making 2SA1864-applied sets small and slim.
- $\cdot \ High \ gain-bandwidth \ product \ f_T.$

Package Dimensions

unit:mm

2106A



Specifications

Absolute Maximum Ratings at Ta = 25°C

Parameter	Symbol	Conditions	Ratings	Unit
Collector-to-Base Voltage	V _{CPO}		-15	V
Collector-to-Emitter Voltage	VCEO		-15	V
Emitter-to-Base Voltage	V _{∑BO}		-10	V
Input Voltage	V _{IN}		-14	V
Collector Current	Ic		-100	mA
Colletor Current (Pulse)	ICO.		-200	mA
Base Current	'в		-20	mA
Collector Dissipation	O.C	//	150	mW
Junction Temperature	1,	7	150	°C
Storage Temperature	Tstg		−55 to +150	°C

Electrical Characteristics at Ta 25 C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Office
Collector Cutoff Current	ICPO	V _{CB} =-10V, I _E =0			-0.1	μΑ
Collector Cutoff Current	ICEO	V _{CB} =-10V, I _E =0			-0.5	μΑ
Emitter Cutoff Currer,t	I _{EBO}	V _{EB} =-5V, I _C =0	-4 10	-532	-760	μΑ
DC Current Gain	h _{FE}	V _{CE} =-2V, I _C =-20mA	50			
Gain-Bandw'dth Product	f _{T*}	V _{CE} =-5V, I _C =-10mA		600		MHz
Output Capacitance	C _{ob*}	V _{CB} =-10V, f=1MHz		0.9		pF

*: Characteristic of ns. ont transistor

Marking . AA

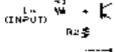
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SANYO Electric Co.,Ltd. Semiconductor Bussiness Headquaters

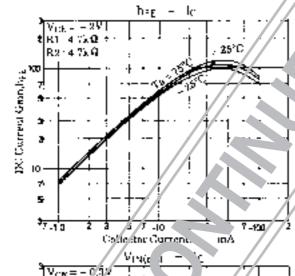
Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	Offic
Collector-to-Emitter Saturation Voltage	V _{CE(sat)}	I _C =-5mA, I _B =-0.5mA		-30	-100	mV
Collector-to-Base Breakdown Voltage	V(BR)CBO	I _C =-10μA, I _E =0	-15			V
Collector-to-Emitter Breakdown Voltage	V(BR)CEO	I _C =-1mA, R _{BE} =∞	-15			V
Input OFF-State Voltage	V _{IN(off)}	V _{CE} =-2V, I _C =-100μA	- J.8	-1.2	-1.5	V
Input ON-State Voltage	V _{IN(on)}	V _{CE} =-0.3V, I _C =-20mA	-1.0	-2.1	-4.0	V
Input Resistance	R1		3.3	1.7	6.1	kΩ
Resistance Ratio	R1/R2		0.9	1.0	1.1	
ON Resistnace	Ron	V _{IN} =-5V, f=1MHz		5.0		Ω

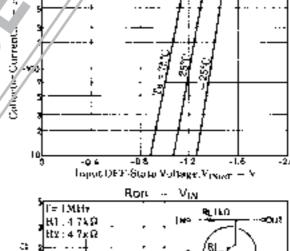




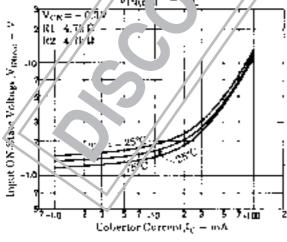


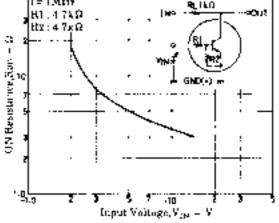


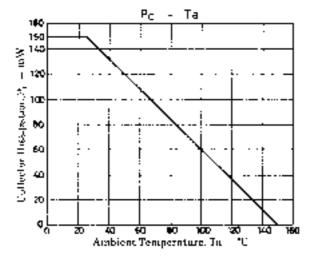




V₍₁₁ = 2) tl +7) 0 R2 +71/1 ViN(off)







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